

CIR-V5DASB4816G

DDR5 Registered DIMM 4800MHz 16GB

Description

This specification defines the electrical and mechanical requirements for 288-pin, 1.1 Volt (VDD and VDDQ), Double Data Rate (DDR), Synchronous DRAM Dual In-Line Memory Modules (DIMM).

CIR-V5DASB4816G is intended for use in server, workstation, and database environments.

Specifications

Density	16GB
Pin Count	288pin
Type	Registered
Dimensions	133.35mm x 31.25mm
ECC	with ECC
Component Config	2G x 8 bit
Data Rate	4800 MHz
CAS Latency	40
Voltage	1.1V
PCB Layers	8
Operating Temp.(TCASE)	0°C~+85°C
Module Ranks	Single Rank

Features

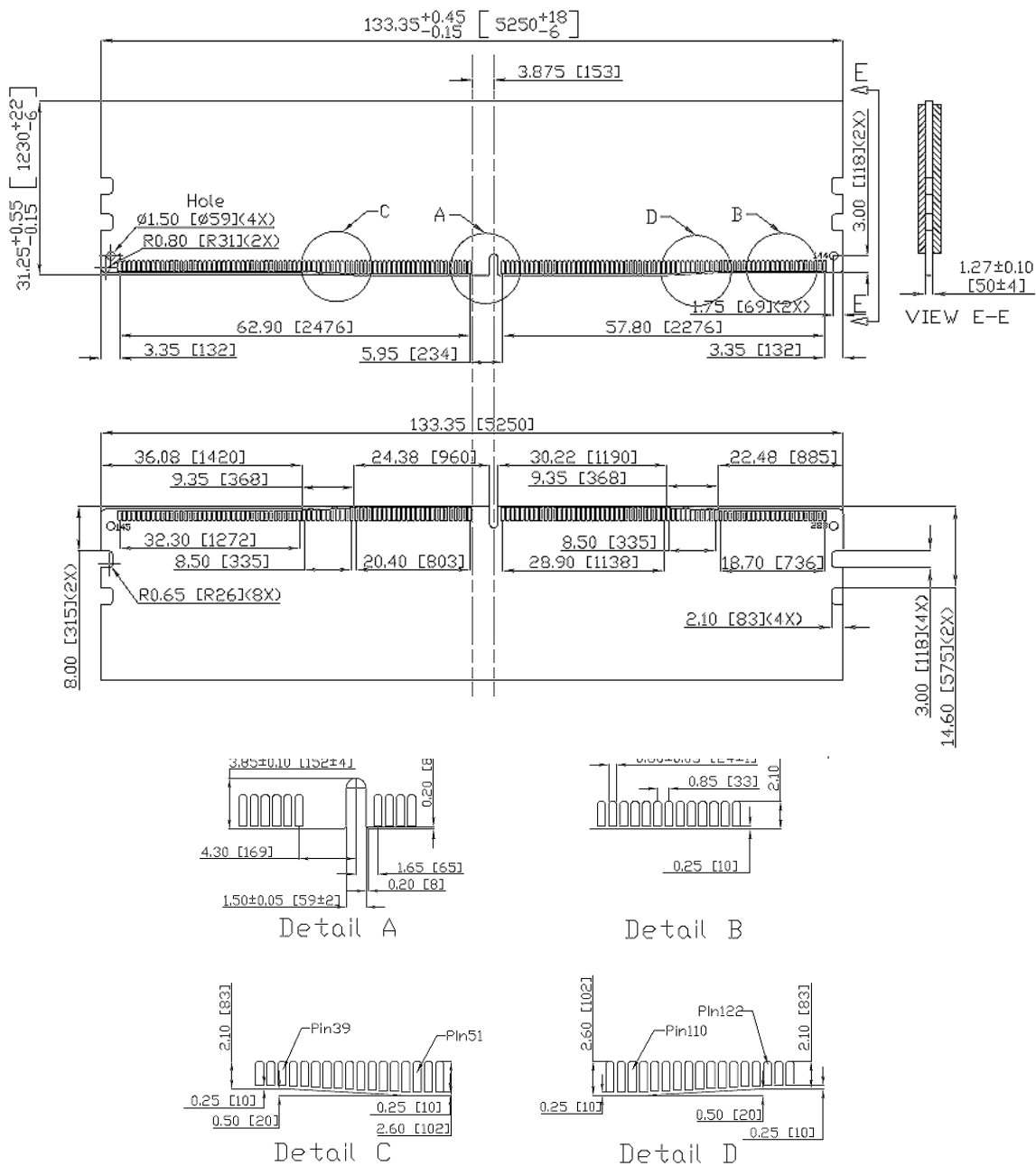
- JEDEC Standard 288-pin Registered Dual In-Line Memory Module
- VDD = VDDQ = 1.1V (1.067V~1.166V)
- VPP = VDDSPD =1.8V
- Programmable /CAS Latency: 22,26,28,30,32,36,40,42
- On-die, internal, adjustable VREF generation for DQ,CA,CS
- 16n-bit prefetch
- Two independent I/O sub channels
- On-Die ECC
- PMIC on DIMM
- SPD EEPROM Hub and Integrated Thermal Sensor
- Fly-By topology
- Terminated control, command and address bus
- RoHS Compliant and Halogen free

Speed Grade

Frequency Grade	Data Transfer Rate	CAS Latency Support			CL-tRCD-tRP
		CL40	TBD	TBD	
DDR5-4800	PC5-38400	4800	TBD	TBD	40-39-39

Package Dimensions

Unit: mm



Tolerances: ± 0.15 mm unless otherwise specified